

Amendments to the Claims

Claims 1-5 (*Cancelled*)

6. (*Currently Amended*) A circuit, comprising: a first chopper having a pair of input terminals and a pair of output terminals; a first differential amplifier having a pair of input terminals coupled to the pair of output terminals of the first chopper, the first amplifier further having a pair of output terminals; a second differential amplifier having a pair of input terminals coupled to the pair of output terminals of the first amplifier, the second amplifier further having a pair of output terminals; a second chopper having a pair of input terminals coupled to the pair of output terminals of the second amplifier, the second chopper further having a pair of output terminals; and a third chopper ~~(206)~~ having a pair of input terminals coupled to the output terminals of the second chopper and a pair of output terminals coupled to the input terminals of the second amplifier; wherein the third chopper is adapted to adjust the phase of a differential feedback signal.

7. (*Original*) The circuit of Claim 6, further comprising at least one capacitor connected between a first output terminal of the third chopper and a first input terminal of the second differential amplifier; and at least one capacitor connected between a second output terminal of third chopper and a second input terminal of the second differential amplifier.

8. (*Original*) The circuit of Claim 7, wherein the third chopper is adapted to adjust the phase of the feedback signal by 180 degrees

9. (*Original*) The circuit of Claim 7, wherein the first, second, and third choppers are adapted to operate synchronously with each other.

10. (*Original*) The circuit of Claim 9, wherein the first, second, and third choppers comprise FETs.

11. (*Original*) The circuit of Claim 7, wherein the capacitors comprise FETs.